

ABSTRACT OF THE DISCLOSURE

A PWM signal is made to rise synchronously with a pixel clock by inputting of a pulse to a set terminal (S terminal) of an R-S flip-flop. One of a reference clock, which is generated by a reference clock generator, and a delay clock, which is obtained by a delay circuit delaying the reference clock, is selected in accordance with delay selection data. The PWM signal is made to fall synchronously with a selected clock.